## Logic Gates

Every engineer will at one point or another be acquainted with the concept of logic gates. These small devices are used to allow "logic" to be introduced into any circuit! Some can be confusing at first but all it takes is a little bit of practice. Below are the most commonly used logic gates and examples of where they are used. Each gate will be accompanied with a truth table, which is a handy way of seeing the different inputs and outputs of a gate (1 is on, 0 is off).



=	& ND-IE	-
_	ND-US	)–

Α	Output
0	1
1	0

Α	В	Output
0	0	0
0	1	0
1	0	0
1	1	1

An inverter or NOT gate inverts a signal. For example, imagine a light switch where flipping it up is off and flipping it down is on.

An AND gate only results in 1 or ON when both inputs are on. Imagine having to flip two light switches for a light to be turned on.

An OR gate allows a current to pass as long as any switch is turned on. This is like having two light



Α	В	Output
0	0	0
0	1	1
1	0	1
1	1	1

switches in a room and as long as one of them is on, the light will be on.



Α	В	Output
0	0	0
0	1	1
1	0	1
1	1	0



Α	Output
0	0
1	1

A XOR gate is only turned ON when only ONE of the inputs are ON. If both are on or off it outputs 0. They can be used to construct computation circuits.

A buffer is essentially the opposite of an inverter. It outputs the same signal but BOOSTS the STRENGTH of the signal.





Α	В	Output
0	0	1
0	1	1
1	0	1
1	1	0

Α В 0

0

0 1

1 0 Output

A NOR gate does exactly the opposite of the OR gate. ONLY when BOTH inputs are OFF does the signal output ON.



1	1	0	
Α	В	Output	
0	0	1	
0	1	0	
1	0	0	
1	1	1	

1

0

0

## **FAU Courses**

EGN1002 – Fundamentals of Engineering EEL3111 – Circuits I CDA 3201C – Logic Design EEE3300 – Electronics 1



## Sources

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